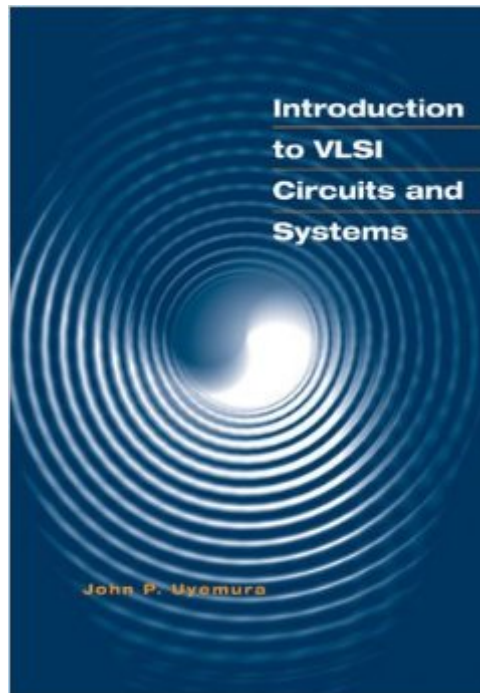


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# Introduction To VLSI Circuits And Systems



## Synopsis

Presents modern CMOS logic circuits, fabrication, and layout in a cohesive manner that links the material together with the system-level considerations. \* Chapter on Verilog HDL allows for rapid start-up. \* Illustrates the top-down design procedure used in modern VLSI chip design with an emphasis on variations in the HDL, logic, circuits and layout.

## Book Information

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## Customer Reviews

If you want to jump into VLSI and get VERY far in one semester, this book is great. You will need Cadence and I also recommend as a challenge to yourself constructing your own 8-bit, 8 word ALU with 3 control bits and 8 operations  $A+BA-BA--A++A$  nand  $BA$  xor  $BA'A$  nor  $Bl$  it will be a good learning experience, specially if you try to optimize it and add a barrel shifter with a 2 clock operation cycle.

I am an EE PhD student in my first year and have to design a mixed analog/digital chip. This book is one of the best technical books I have ever read. Well structured, thorough but does not give you so much detail that you risk drowning. This book actually is fun to read.

Currently using this book for a class in VLSI design. Book is well written and easy to read. Stress fundamentals and design concepts. Highly recommended!

This book is very well written and easy to understand. The author did an excellent job organizing the topics and explaining the key points thoroughly.

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